

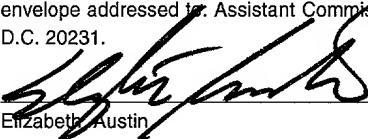
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alan Gatherer, Et al. Docket No.: TI-30024  
Serial No.: TBD Examiner: TBD  
Filed: January 24, 2001 Art Unit: TBD  
For: Map Decoding With Parallelized Sliding Window Processing

**PRELIMINARY AMENDMENT**

January 29, 2001

Assistant Commissioner For Patents  
Washington, D. C. 20231

<b>MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)</b>	
I hereby certify that on or before this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner For Patents, Washington, D.C. 20231.	
 Elizabeth Austin	<u>1/29/01</u> Date

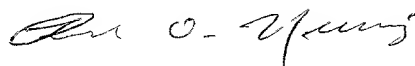
Sir:

Please amend the above-referenced application as follows:

**In the Specification:**

On page 1, after the title and before the first line of text, insert a new paragraph --This application claims priority under 35 U.S.C. §119(e)(1) of provisional application Serial No. 60/179,055, filed on 01/31/2000, the entirety of which is incorporated herein by reference.--.

Respectfully submitted,



Ronald O. Neerings  
Attorney for Applicants  
Reg. No.: 34,227

Texas Instruments Incorporated  
P. O. Box 655474, MS 3999  
Dallas, Texas 75265  
(972) 917-5299